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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Averett, Guy E. et al.

Serial No.:

10/072,145

Filing Date:

02/07/2002

Group Art Unit:

2811

Examiner:

Magee, Thomas J.

Title:

SEMICONDUCTOR DEVICE AND METHOD OF

PROVIDING REGIONS OF LOW SUBSTRATE

CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER Thomas J. Mageo AT THE FAX NO. (703) 872-9318

Tuoman .	at mages the	
	January 15,2003	
OM:	Date of Facsimile Transmission	20 page
	Lydia McNamara	V
	Name of Person Signing Certificate	1
	Lessin mchamara	1/15/03
	SIGNATURE	DATE

Honorable Commissioner of Patents, Washington, D.C. 20231

FAX RECEIVED

JAN 1 5 2003

AMENDMENT

TECHNOLOGY CENTER 2800

SIR:

In response to the office action mailed November 20, 2002, please amend the above-identified application as follows.